



## Computer Architecture With Synergistic Heterogeneous Processors

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### The Invention

A computer architecture employs multiple special-purpose processors having different affinities for program execution to execute substantial portions of general-purpose programs to provide improved performance with respect to a general-purpose processor executing the general-purpose program alone.

### Additional Information

#### For More Information About the Inventors

- [Karthikeyan Sankaralingam](#)

#### Tech Fields

- [Information Technology : Hardware](#)

For current licensing status, please contact Jeanine Burmania at [jeanine@warf.org](mailto:jeanine@warf.org) or 608-960-9846