



## Adaptive Computer Cache Architecture

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### The Invention

A high-bandwidth adaptive cache reduces unnecessary cache accesses by providing a laundry counter indicating whether a given adaptive cache region has any dirty frames to allow write-back without a preparatory adaptive cache read. An optional laundry list allows the preparatory adaptive cache read to also be avoided if the tags of the data being written back match all tags of the data in the adaptive cache that is dirty.

### Additional Information

#### For More Information About the Inventors

- [Mark Hill](#)

#### Tech Fields

- [Information Technology : Hardware](#)

For current licensing status, please contact Emily Bauer at [emily@warf.org](mailto:emily@warf.org) or 608-960-9842