



Computer Architecture For High Speed, Graph-Traversal

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The Invention

A computer architecture for graph-traversal provides a processor for bottom-up sequencing through the graph data according to vertex degree. This ordered sequencing reduces redundant edge checks. In one embodiment, vertex adjacency data describing the graph may be allocated among different memory structures in the memory hierarchy to provide faster access to vertex data associated with vertices of higher degree reducing data access time. The adjacency data also may be coded to provide higher compression in memory of vertex data having high vertex degree.

Tech Fields

- [Information Technology : Computing methods, software & machine learning](#)
- [Information Technology : Hardware](#)

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