



High-Speed Graph Processor for Large Scale Data Structures

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The Invention

A computer architecture for graph processing employs a high-bandwidth memory closely coupled to independent processing elements for searching through a graph using a first set of processing elements operating simultaneously to determine neighbors to a current frontier and second processing elements operating simultaneously to determine a next frontier, this process being repeated to search through graph nodes.

Tech Fields

- [Information Technology: Hardware](#)

For current licensing status, please contact Jeanine Burmania at jeanine@warf.org or 608-960-9846