



Integration Of Semiconductor Membranes With Piezoelectric Substrates

[View U.S. Patent No. 11,864,465 in PDF format.](#)

WARF: P200105US01

Inventors: Max Lagally, Abhishek Bhat, Frank Flack, Shelley Scott, Robert Blick

The Invention

Piezoelectrically actuated devices constructed from thin semiconductor membranes bonded directly to piezoelectric substrates are provided. Methods for fabricating these devices are also provided. The bonding of the semiconductor to the piezoelectric material does not require the use of any intermediate layers, such as bonding agents.

Additional Information

For More Information About the Inventors

- [Max Lagally](#)

Tech Fields

- [Semiconductors & Integrated Circuits : Design & fabrication](#)

For current licensing status, please contact Jeanine Burmania at jeanine@warf.org or 608-960-9846