

SUBSTRATE MODIFICATIONS TO SUPPRESS CORRELATED ERRORS IN MULTIQUBIT ARRAYS

WARF: P210154US02

Inventors: Robert McDermott, Mark Eriksson

The Invention

UW Madison researchers have developed Qubit arrays having substrates that are engineered to suppress correlated dephasing errors, correlated relaxation errors, or both. The engineered substrates can be used to suppress noise and correlated errors in any qubit array that suffers from charge fluctuations or other noise that creates a non-equilibrium, error-producing state in the qubit array.

Additional Information

For More Information About the Inventors

• Robert McDermott

Publications

· Read an article about this technology.

Tech Fields

• Information Technology: Hardware

For current licensing status, please contact Emily Bauer at emily@warf.org or 608-960-9842

