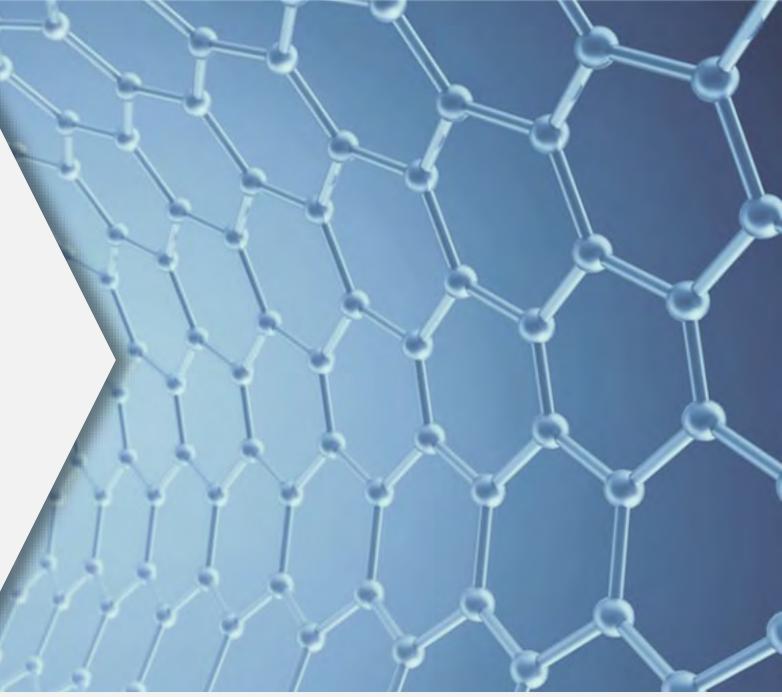


Unlocking Nature's Perfect Semiconductor for Next-Gen Electronics



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Growth of Next-Gen Wireless





Current Materials Cannot Meet Needs





Next Generation RF Material Requirements

- Operate at higher frequencies and lower power
- Integrate directly with computing/logic

Current materials silicon and gallium arsenide (GaAs) are unable to meet needs

<u>Silicon</u>

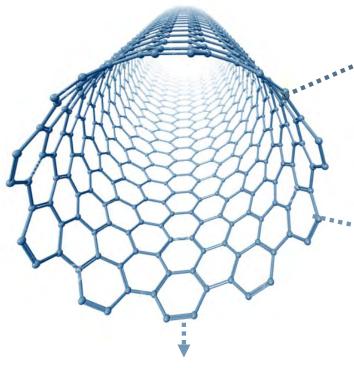
- Logic/computing material
- Direct integration
- Cheap
- Low frequencies

<u>GaAs</u>

- High-performance RF material
- Can't integrate directly with Si
- Costly
- High frequencies

Ideal Solution: Carbon nanotubes on silicon

Layer of carbon atoms rolled into a cylinder



Sensors/Displays

- 10-100× faster performance

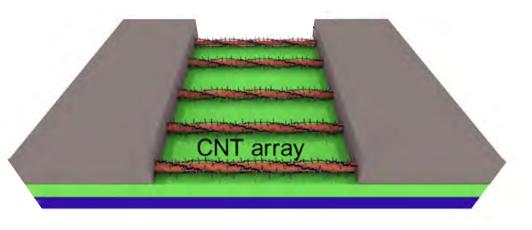
SixLine's 1st Target: **Radiofrequency (RF)**

- 3-8× higher frequencies
- Larger data bandwidths
- On-chip integration with Si
- Intrinsic data quality, low noise at high frequency

Logic/Computing

- 5-20× lower energy consumption (2D FETs)
- Enables 3D circuits—route to advance Moore's Law
- 100 1000× lower energy ulletconsumption (3D FETs)

Carbon nanotubes must be organized into densely packed aligned arrays of multiple nanotubes to exploit their properties



Mechanically resilient Semiconductor energy consumption is supply limited to

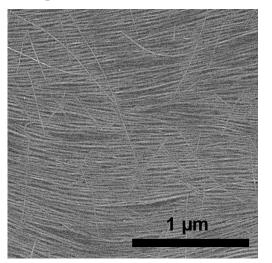
25% consumption of prime energy by 2040

Aligning Nanotubes: A 30-Year Challenge

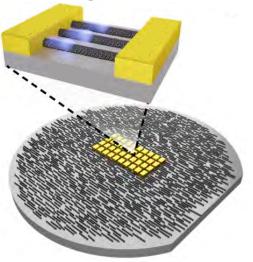




Aligned nanotube film



Next-gen devices



Transforming random nanotube soot into highquality nanotube arrays using a scalable process for highperformance electronics

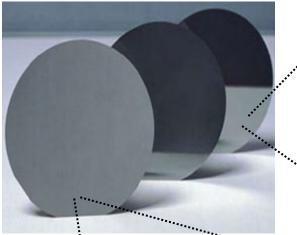


Ideal Aligned Nanotube Arrays

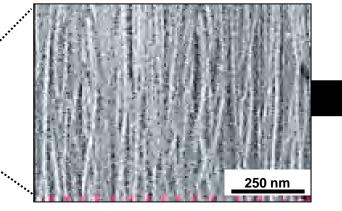


Highly-aligned, registered arrays of semiconducting nanotubes from an industry scalable process

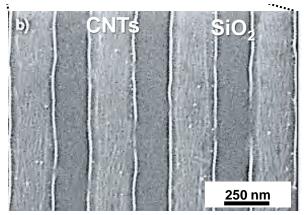
Industry scalable



Highly aligned (±7°) Nanotubes



Registered nanotube placement



- Compatible with conventional fabrication techniques
- ✓ Fast process—alignment occurs within seconds
- ✓ Room temperature/pressure
- $\checkmark\,$ Direct integration on silicon

High-performance nanotube electronic devices Oxide

Value Proposition: Performance & Cost



30 to 60% lower manufacturing cost **Performance/Integration** compared to GaAs 3-8× Higher frequencies 3D integration with logic >50% 250% Larger bandwidths Lower energy consumption 3D LOWER RAW WAFER MORE FAB RF FACILITIES COST GaAs SixLine NextG **3D Logic** 200mm 150mm CMOS Science 378, (2022) **Drop-in tool for** 58% **65%** semiconductor fabs LOWER DIE MFG THROUGHPUT GaAs INCREASE **SixLine** COST 42% of heat / neat / cool handle **SLOW** handle cost 000 300mm Si

Strong Management & Research Team





- Co-inventor of technology
- 10 years technical/materials research experience
- Entrepreneurship training

Prof. Mike Arnold, PhD

Chief Technical Advisor, Co-founder



- Internationally recognized nanotube expert
- Co-inventor of technology
- Professor at UW-Madison

Jeff Moore Strategy/Commercial Support



- >20 years startup experience
- Former COO and CFO at
 Virent
- Raised \$25M+ in nondilutive funding at last startup prior to acquisition



Prof. Padma Gopalan, PhD

Technical Advisor

- Expert in nanotube assembly and chemistry
- Co-inventor of technology
- Professor at UW-Madison

Significant amount of materials, industry, and startup expertise

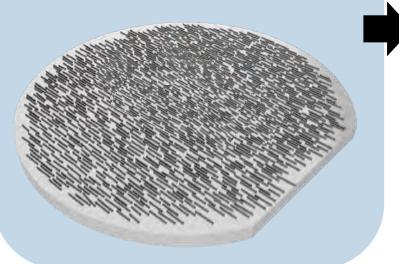
Commercial Product Path



SixLine will develop and sell (1) aligned nanotube films, (2) nanotube alignment process equipment, and (3) nanotube materials

Aligned Nanotube Films

- Accelerate industry transition and seed demand
- Focus on finding early adopters



Nanotube Process Equipment

Performs alignment process and integrates into fab lines



Finding credible partner

Nanotube Materials

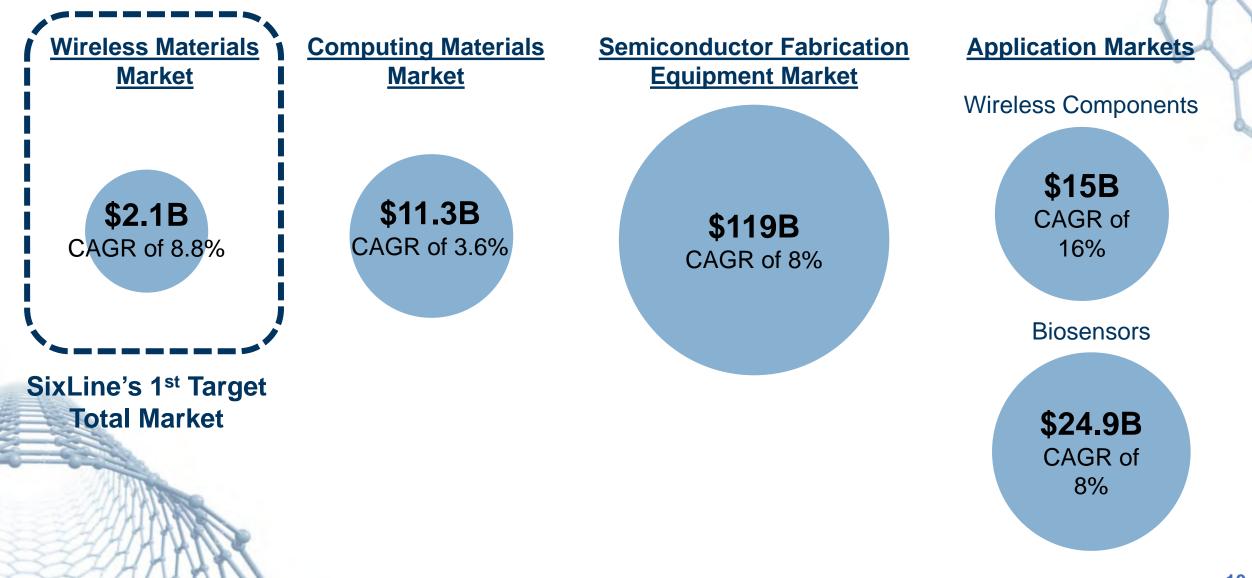
Feedstock for process equipment



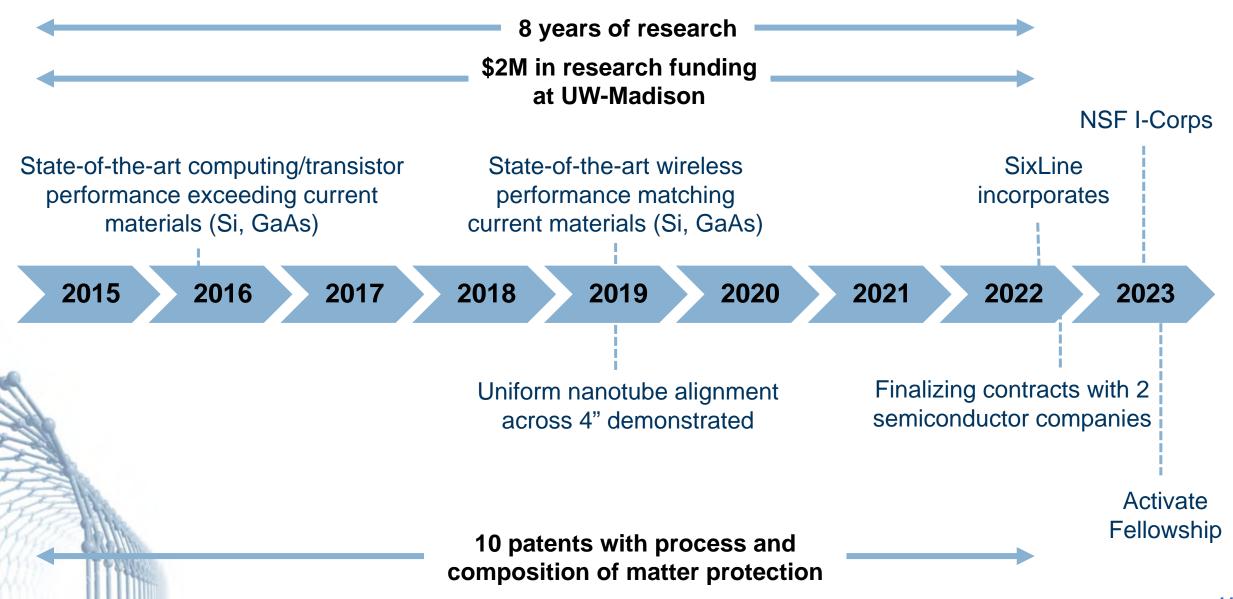
Nanotube materials will maximize recurring SixLine revenue (\$2-20M/customer)

SixLine Tech has Large Potential Markets



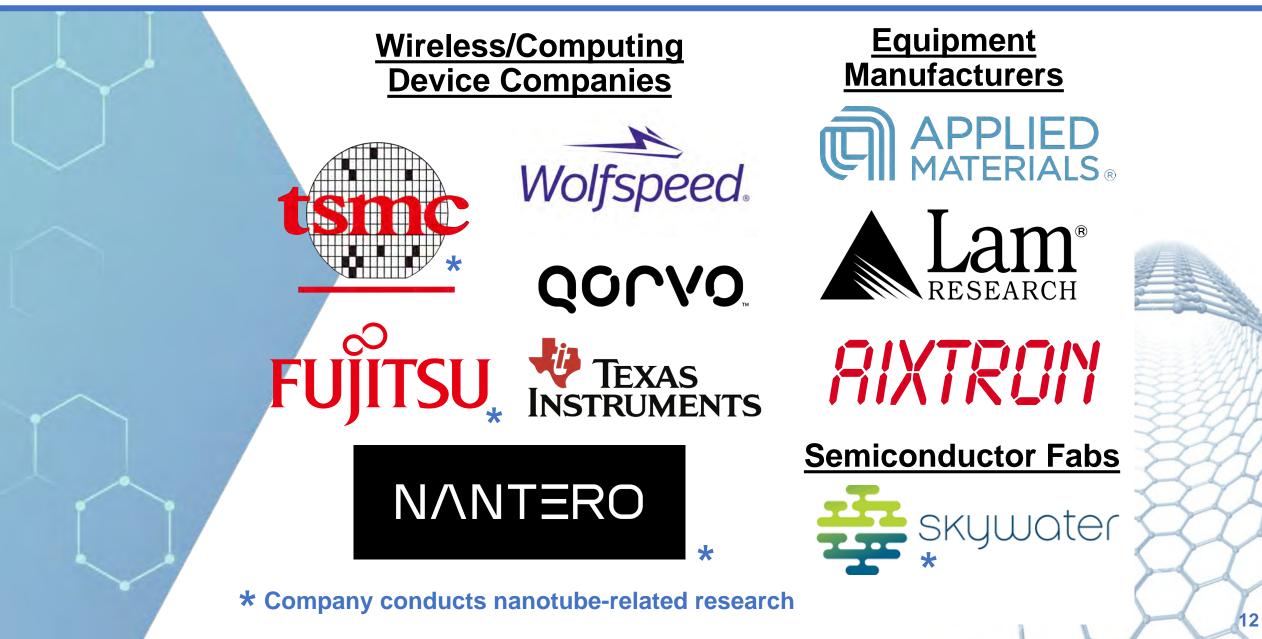


Strong Company & Technology Traction



Exit Strategy







Produces nanotube materials/arrays enabling nextgen electronic performance Overcomes 30-year challenge to leverage aligned carbon nanotubes

- Significant performance and cost advantages
- Comprehensive IP library
- Strong industry engagement
- Broad application across multiple device markets



Unlocking Nature's Perfect Semiconductor for Next-Gen Electronics

Katy Jinkins, Ph.D. Co-founder and CEO

Materials Scientist/Engineer

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